Capacitance Meter Manual

Features
- About 1% accuracy. No calibration needed.
- Measuring range: 1pF - 500µF
- Automatic range select
- Zeroing available
- Real time serial output of measurement read-outs with time stamp
- Low cost and easy to build

How to assemble
It is pretty straightforward to assemble the cap meter. Install all parts included in the kit onto the PCB by following schematic and labels. Double check to ensure no soldering errors. If everything ok then the cap meter can be powered up and put into use. No adjustment needed.

How to Use
Measurement:
Insert the capacitor to be tested into J5. Capacitance will be displayed. Alternatively caps not fitting into J5 can be connected through J3 with a pair of clips. Usually large caps take longer to show results but should not longer than 2 seconds.

Zeroing:
Remove cap (if any) from J5 and J3. Press the “ZERO” button. “C0  ” will display. Wait for it to disappear. Now the stray capacitance is compensated. The compensation value is stored in EEPROM and will remain in effect until another zeroing is performed. To obtain good measurement results for small capacitance shielding is required. To do this put the board into a metal box and connect the ground of the meter to the enclosure.

Serial Data Format:
The ASCII outputs from TXD pin of J4 include sequence number, measurement time (in seconds), and measured capacitance. The baud rate is 38.4Kbps fixed. The data format is 8 data bits, 1 stop bit, and no parity.

Specifications
Error: < 2%
Range: 1pF – 500µF
Power Supply voltage: 8 – 16 DC
Power Supply current: <30mA

Revision History

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<th>Version</th>
<th>Date</th>
<th>Descriptions</th>
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<tbody>
<tr>
<td>01</td>
<td>2008.09.17</td>
<td>First created</td>
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<tr>
<td>02</td>
<td>2008.11.28</td>
<td>Modified for 109-06000-00B PCB. Removed reworking instructions since errors in previous PCB had been corrected.</td>
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